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No. 60/072,561, filed on January 22, 1998, entitled "Codelets," all of which are incorporated herein by reference.

In the Claims:

/ / / / / / / / / / /
 Please amend claims 1, 9, 12-14, 17, 20-22, 25, 33, 39, 41, and 42 as

follows:

Sub B1

1. (Amended) A secure multiple application card system including an IC card comprising a microprocessor, a read-only memory and an electrically erasable programmable read only memory, said system comprising:

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means for manufacturing said IC card and for storing at the time of manufacture in said read-only memory an operating system and programming instructions without an address table with memory addresses of at least one of said programming instructions; and

means for personalizing said IC card after said manufacturing step and for storing at the time of personalization in said electrically erasable programmable read only memory said address table with memory addresses of at least one of said programming instructions,

wherein the operating system will only access those programming instructions in accordance with the addresses indicated in the address table.

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9. (Amended) A process for providing a secure multiple application card system including an IC card comprising a microprocessor, a read-only memory and an electrically erasable programmable read only memory, said process comprising the steps of:

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cont.

manufacturing said IC card and storing at the time of manufacture in said read-only memory an operating system and programming instructions without an address table with memory addresses of at least one of said programming instructions; and

personalizing said IC card after said time of manufacture by storing in said electrically erasable programmable read only memory said address table with memory addresses of at least one of said programming instructions,

wherein the operating system will only access those programming instructions in accordance with the addresses indicated in the address table.

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12. (Amended) The process of claim 9, wherein said step of storing in said electrically erasable programmable read only memory further includes storing additional programming instructions.

13. (Amended) The process of claim 12, wherein said additional programming instructions comprise at least one primitive.

14. (Amended) The process of claim 12, wherein said additional programming instructions comprise at least one codelet.

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17. (Amended) A process for providing a secure multiple application card comprising a microprocessor, a first memory and a second memory, said process comprising the steps of:

manufacturing said card and storing at the time of manufacture in said first memory an operating system and programming instructions without an address table with memory addresses of at least one of said programming instructions; and

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personalizing said IC card after said storing step by storing in said second memory said address table with memory addresses of at least one of said programming instructions;

wherein said operating system will only access those programming instructions in accordance with the addresses indicated in the address table.

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✓20. (Amended) The process of claim 17, wherein said step of storing in said electrically erasable programmable read only memory further includes storing additional programming instructions.

✓21. (Amended) The process of claim 20, wherein said additional programming instructions comprise at least one primitive.

✓22. (Amended) The process of claim 20, wherein said additional programming instructions comprise at least one codelet.

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25. (Amended) A secure multiple application card system including an IC card comprising a microprocessor, a read-only memory and an electrically erasable programmable read only memory, said system comprising:

means for manufacturing said IC card and for storing at the time of manufacture in said read-only memory an operating system and programming instructions; and

means for personalizing said IC card after the time of manufacture and for storing at the time of personalization in said electrically erasable programmable read only memory an address table with memory addresses of at least one of said programming

instructions,

wherein the operating system will only access those programming instructions in accordance with the addresses indicated in the address table;

and wherein said means for personalizing said IC card can be operated to store additional programming instructions in said read-only memory and includes means for inserting addresses for said additional programming instructions in said address table.

33. (Amended) A process for providing a secure multiple application card system including an IC card comprising a microprocessor, a read-only memory and an electrically erasable programmable read only memory, said process comprising the steps of:

manufacturing said IC card and storing at the time of manufacture in said read-only memory an operating system and programming instructions;

personalizing said IC card after said time of manufacture by storing in said electrically erasable programmable read only memory an address table with memory addresses of at least one of said programming instructions,

wherein the operating system will only access those programming instructions in accordance with the addresses indicated in the address table;

storing additional programming instructions in said read-only memory;

and

inserting addresses for said additional programming instructions in said address table.

39. (Amended) A process for providing a secure multiple application card comprising a microprocessor, a first memory and a second memory, said process comprising the steps of:

a. storing in said first memory an operating system and programming instructions;

b. personalizing said IC card after said storing step by storing in said second memory an address table with memory addresses of at least one of said programming instructions,

wherein said operating system will only access those programming instructions in accordance with the addresses indicated in the address table;

c. storing additional programming instructions in said read-only memory; and

d. inserting addresses for said additional programming instructions in said address table.

41. (Amended) The process of claim 40, wherein said programming instructions comprise at least one primitive.

✓ 42. (Amended) The process of claim 40, wherein said programming instructions comprise at least one codelet.